

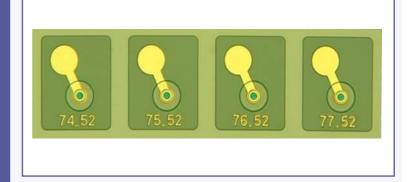
850nm 10Gb/s Multimode VCSEL Chip Array

Features:

- 850nm multimode emission
- Low threshold and operation current
- High reliability
- Low electrical parasitics
- Data rates from DC to 10 Gb/s
- Backside common cathode and topside anode configuration
- Available as 4 and 12 channel array chip
- RoHS compliant

Applications:

- Parallel fiber optical communication links
- Smart cables, HDMI



Oclaro's high speed 850nm multimode VCSEL array is designed to meet stringent specifications for high speed data communications. The high performance, high reliability device is engineered with low electrical parasitics for data rates up to 10Gb/s. The VCSEL operates in multiple transverse and single longitudinal modes and emits a circular symmetric beam with low divergence that can be efficiently coupled into 50/125 and 62.5/125 μ m multimode fibers. The common cathode chip is available in 4 and 12 channel configuration with a pitch of 250 microns between individual channels.

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Electro – Optical Characteristics*

Parameter	Symbol	Conditions	Ratings			1124
rarameter	Symbol	Conditions	Min	Тур	Max	Unit
Threshold current	I _{th}			0.8	1.2	mA
Slope efficiency	η	I=I _{th} +1 mA	0.3	0.5	0.7	mW/mA
Optical output power	Pout	I _{op} = 6.0mA		2.5		mW
Operating voltage	Uop	lop = 6.0mA		2.0		V
Differential resistance	R _d	$I_{op} = 6.0 \text{mA}$		60	85	Ω
Emission wavelength	λ	lop = 6.0mA, T=-10°C - 85°C	840	850	860	nm
Spectral width, RMS	Δλ	l _{op} = 6.0mA			0.65	nm
Beam divergence	Θ	l_{op} = 6.0mA, Full width 1/e ²		26	32	0
Capacitance	С	lop = 6.0mA		0.3	0.5	рF
Modulation bandwidth	f _{3dB}	l _{op} = 6.0mA	9			GHz
Disc /fall time	tr	1 - / 0m A FD-FdD 2007 9007		30	35	ps
Rise/fall time	† _f	l _{op} = 6.0mA, ER=5dB, 20% - 80%		40	45	ps
Relative intensity noise	RIN _{12(OMA)}	lop = 6.0mA, ER=5dB, 7.73GHz BW			-128	dB/Hz
Threshold uniformity	Δl_{th}				0.2	mA
Slope efficiency uniformity	Δη				0.05	mW/mA

Thermal Characteristics

Parameter	Symbol		Unit			
raidillelei	Symbol	Min	Тур	Max	Offin	
Wavelength tuning coefficient	δλ/δΤ		0.06		nm/K	
Threshold current variation -10°C - 85°C	$\Delta l_{th,T}$			1.2	mA	
Slope efficiency variation -10°C - 85°C	$\Delta\eta_{\text{T}}$		-0.4		%/K	
Thermal impedance	Z _{th}		2.5		K/mW	

^{*}T=25°C unless otherwise noted

Absolute Maximum Ratings

Parameter	Rating	Unit
Optical output power	6	mW
Peak forward current	12	mA
VCSEL reverse voltage	5	٧
Operating temperature	-10 to +85	°C
Storage temperature	-40 to +100	°C
Mounting temperature (max. 1h)	165	°C

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Data Sheet



Chip Dimensions

Parameter	Min	Тур	Max	Unit
Die length, APA7301040000	960	980	1000	μm
Die length, APA7301120000	2960	2980	3000	μm
Die width	260	280	300	μm
Die height	135	150	165	μm

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Oclaro is fully committed to environment protection and sustainable development and has set in place a comprehensive program for removing polluting and hazardous substances from all of its products. The relevant evidence of RoHS compliance is held as part of our controlled documentation for each of our compliant products. RoHS compliance parts are available to order, please refer to the ordering information section for further details.

Ordering Information:

Product Code	Data Rate	Description
APA7301040000	10Gb/s	850nm 10G MM 1x4 VCSEL array
APA7301120000	10Gb/s	850nm 10G MM 1x12 VCSEL array

Contact Information

www.oclaro.com

Important Notice

Performance figures, data and any illustrative material provided in this data sheet are typical and must be specifically confirmed in writing by Oclaro before they become applicable to any particular order or contract. In accordance with the Oclaro policy of continuous improvement specifications may change without notice. Further details are available from any Oclaro sales representative.









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